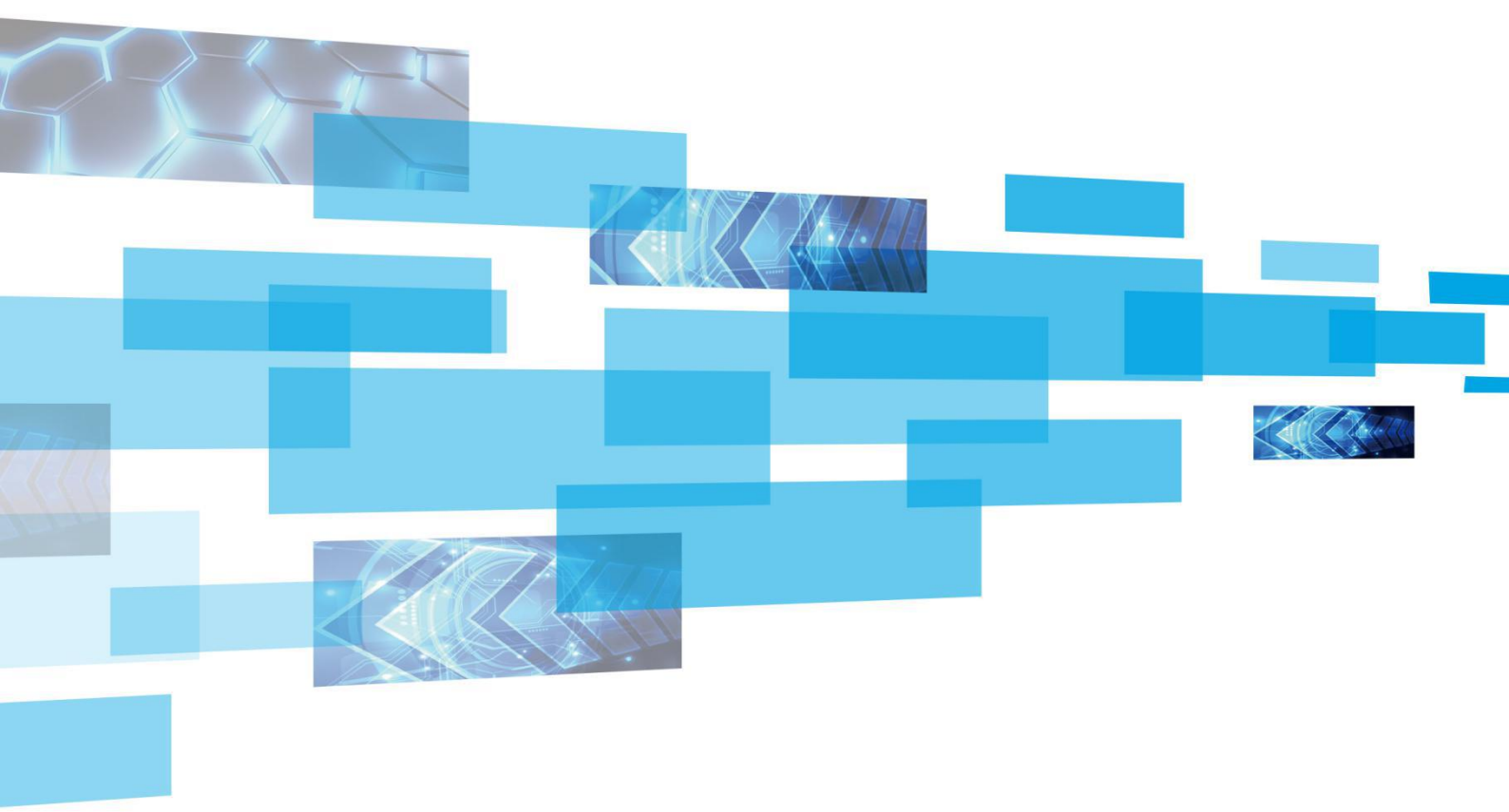




Universal Receiver

D90



Product Data-sheet

Version No.1.0

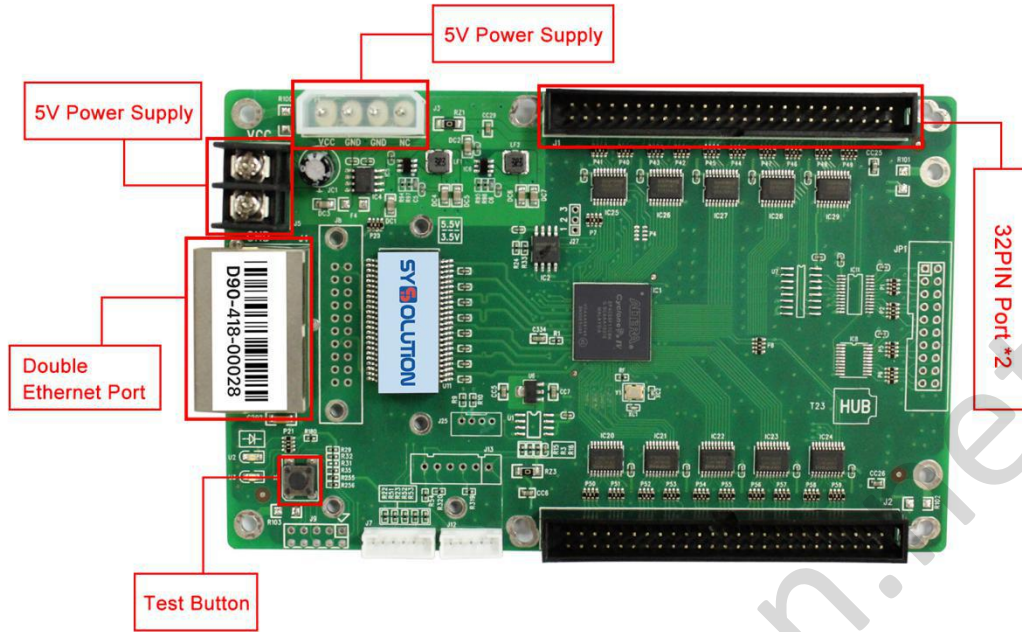
Document ID:SYS/C3-0103005085

Update Record

No.	Version No.	Updates	Revision Date
1	1.0	Initial issue	2018.04.01

www.sysolution.net

Appearance

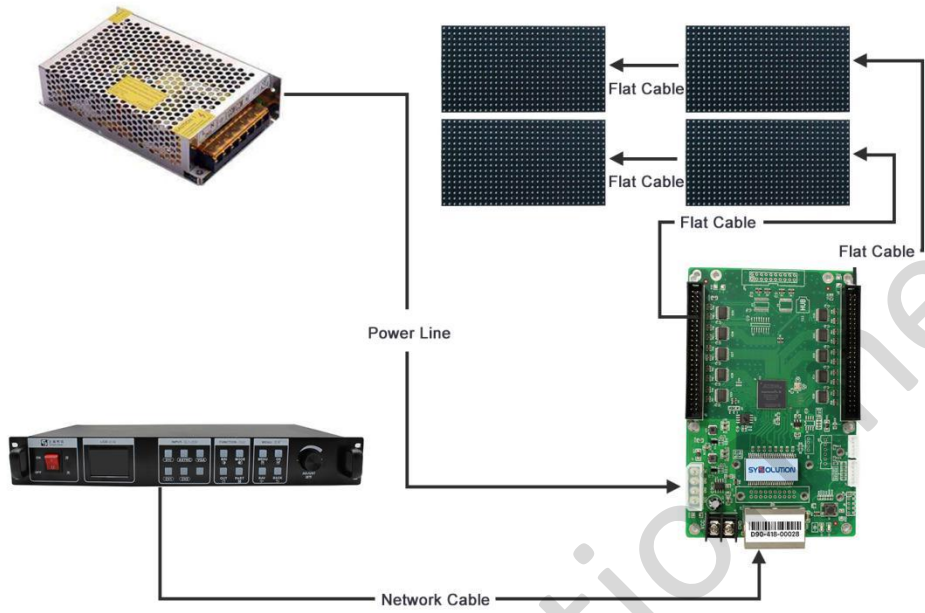


Univalis Receiver D90

Technical Parameters

Performance Optional	Typical Data	Maximum
LED Module	Single/Double/Full color Real/Virtual Pixel	
Number of Receiving Cards by Single Cable Cascade	<1000	1000
Single Receiving Cards with Pixel Area	<96000 Pixel	
RGB Group No. With Single Receiving Cards Output	24	24
One RGB Drive Lines	1/2/4/8/16/32	1~32
Fiber Transmission Distance	Multimode Fiber :500Meter ; Single Mode Fiber 10 Kilometer	
Other Performance	10b Video source 、 LED Self-test OE Polar Toggle Switch Control protection Scanning Screen Line	
Work Current	0.6A	1.0A
Work Temperature	-10°C - 65°C	
Work Temperature Limit	-20°C - 75°C	
Operating Humidity (%)	0%-95%	

Connection



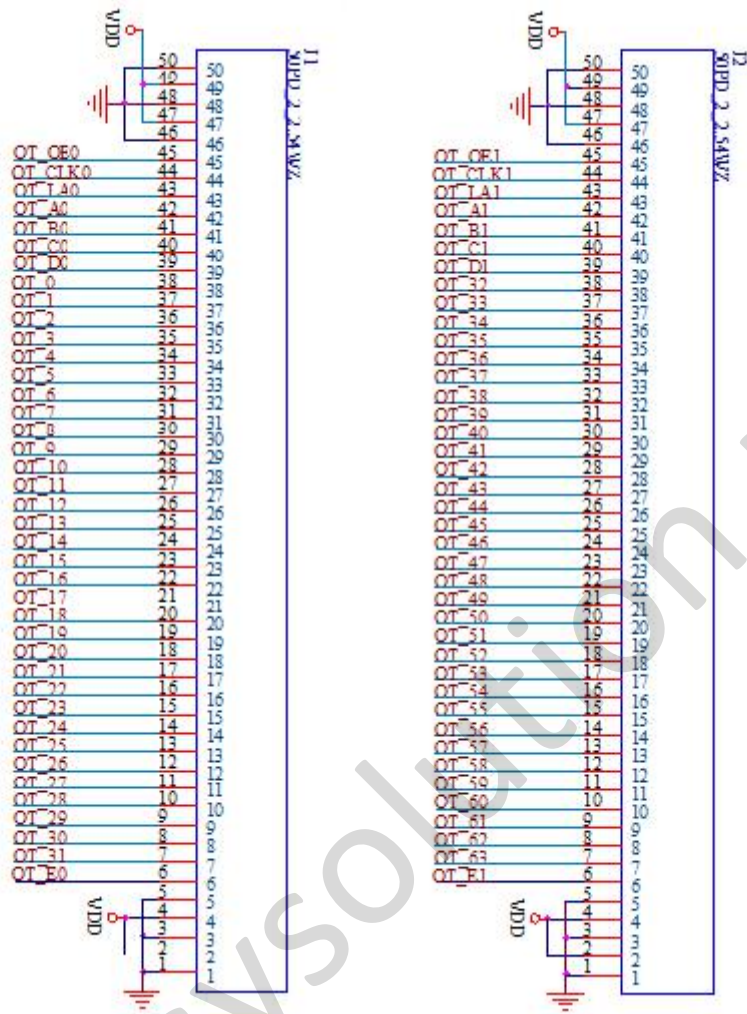
Performances

D90 is the conventional universal receiving card launched by Shanghaixixun.

It has the following characters:

1. Support 21 set of RGB output.
2. Input image gravel level support 8/10 BIT Mode.
3. Output gray level can reach up to 20 BIT.
4. The Max pixel of one single is 96K.
5. The input voltage is 3.5V-5.5v.
6. High refresh. High brightness.
7. Support parameter read back.
8. Network line double backup function, support line detection.
9. Support cabinet temperature, humidity, power voltage monitoring and fan control.
10. Support brightness, chroma, point by point correction.
11. Range of supported drive IC is wide. Such as PWM IC, Point by point Detecting IC and conventional IC.
12. Support prestorage screen settings.
13. Support parameters monitoring functions.
14. Support outside environment monitoring functions.
15. Each interface provides a power supply pin, support unit power to take power.
16. Comply with RoHs Standards.
17. Comply with CE-EMC standards.

Definition of Output Port



JP1 Definition:

Definition	Pin	Pin	Definition
GND	1	2	VCC
GND	3	4	VCC
GND	5	6	OUTE0
OUT_31	7	8	OUT_30
OUT_29	9	10	OUT_28
OUT_27	11	12	OUT_26
OUT_25	13	14	OUT_24

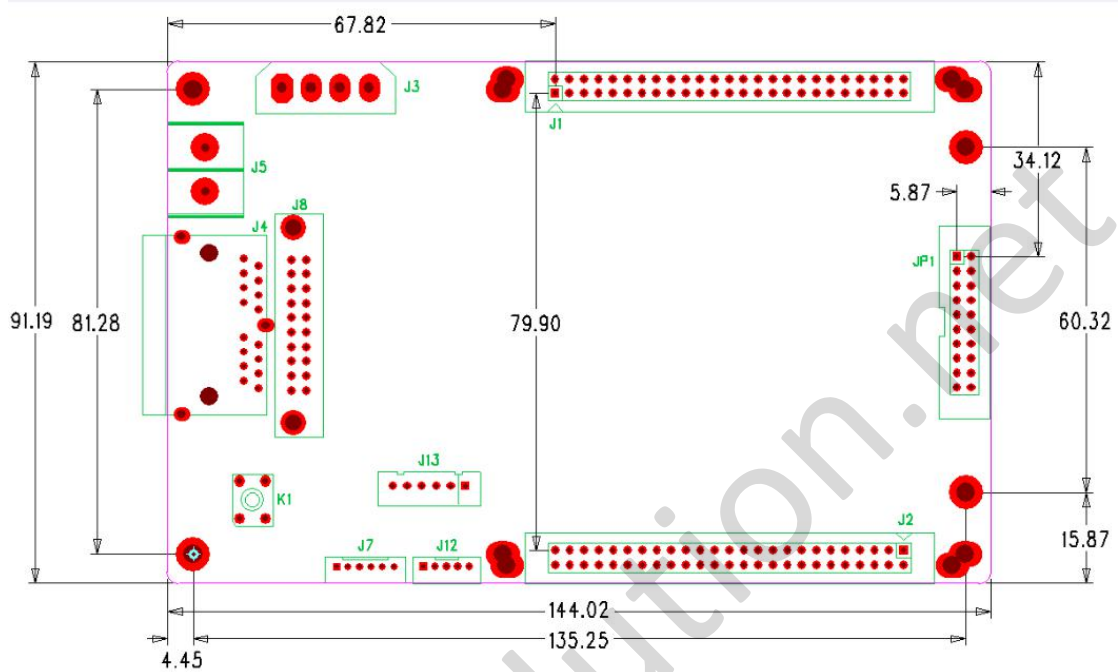
OUT_23	15	16	OUT_22
OUT_21	17	18	OUT_20
OUT_19	19	20	OUT_18
OUT_17	21	22	OUT_16
OUT_15	23	24	OUT_14
OUT_13	25	26	OUT_12
OUT_11	27	28	OUT_10
OUT_9	29	30	OUT_8
OUT_7	31	32	OUT_6
OUT_5	33	34	OUT_4
OUT_3	35	36	OUT_2
OUT_1	37	38	OUT_0
OUT_D0	39	40	OUT_C0
OUT_B0	41	42	OUT_A0
OUT_LA0	43	44	OUT_CLK0
OUT_OE0	45	46	GND
VCC	47	48	GND
VCC	49	50	GND

JP2 Definition:

Definition	Pin	Pin	Definition
GND	1	2	VCC
GND	3	4	VCC
GND	5	6	OUT_E1
OUT_63	7	8	OUT_62

OUT_61	9	10	OUT_60
OUT_59	11	12	OUT_58
OUT_57	13	14	OUT_56
OUT_55	15	16	OUT_54
OUT_53	17	18	OUT_52
OUT_51	19	20	OUT_50
OUT_49	21	22	OUT_48
OUT_47	23	24	OUT_46
OUT_45	25	26	OUT_44
OUT_43	27	28	OUT_42
OUT_41	29	30	OUT_40
OUT_39	31	32	OUT_38
OUT_37	33	34	OUT_36
OUT_35	35	36	OUT_34
OUT_33	37	38	OUT_32
OUT_D1	39	40	OUT_C1
OUT_B1	41	42	OUT_A1
OUT_LA1	43	44	OUT_CLK1
OUT_OE1	45	46	GND
VCC	47	48	GND
VCC	49	50	GND

Dimension and Connector Definition



Notes

1. Please follow this instruction exactly.
2. Professionals are needed to install and test the product and has to be anti-static.
3. Keep away from water.

